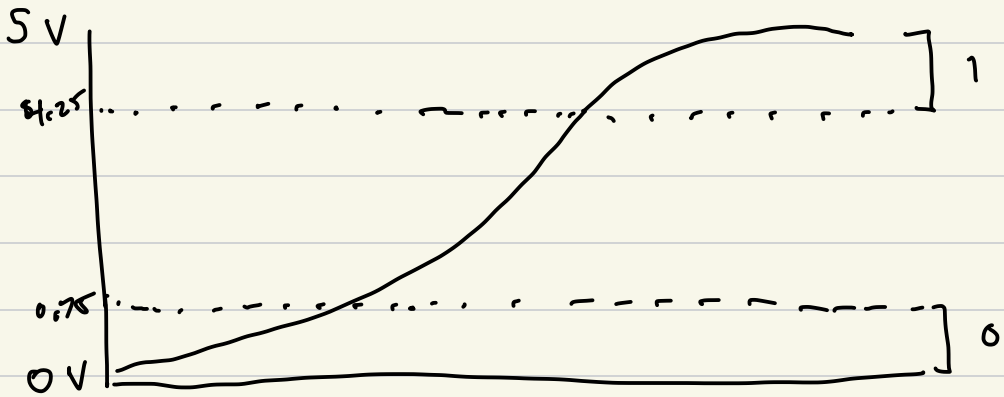


# CS315-02 Lab Intro to Digital Design

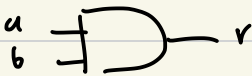
## Digital Design

### Analog $\rightarrow$ Digital



Wires  $\rightarrow$  devices  $\rightarrow$  gates

AND



Code

$$r = a \& b$$

Boolean Algebra

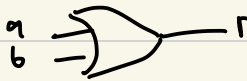
$$r = a \cdot b$$

Logic

$$r = a \wedge b$$

| a | b | r |
|---|---|---|
| 0 | 0 | 0 |
| 0 | 1 | 0 |
| 1 | 0 | 0 |
| 1 | 1 | 1 |

OR



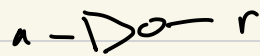
$$r = a | b$$

$$r = a + b$$

$$r = a \vee b$$

| a | b | r |
|---|---|---|
| 0 | 0 | 0 |
| 0 | 1 | 1 |
| 1 | 0 | 1 |
| 1 | 1 | 1 |

NOT

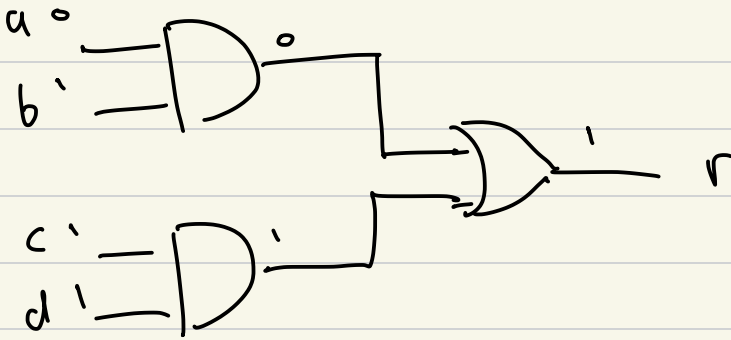


$$r = \neg a$$

$$r = \bar{a}$$

$$r = \neg a$$

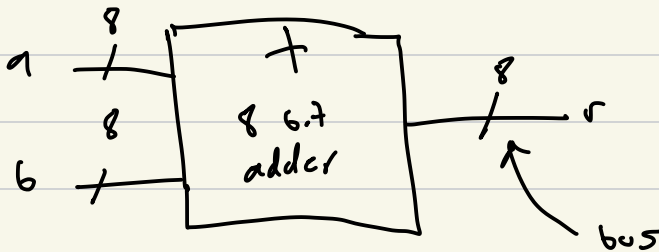
| a | r |
|---|---|
| 0 | 1 |
| 1 | 0 |



$$\begin{aligned}
 r &= (a \cdot b) + (c \cdot d) \\
 &= 0 \cdot 1 + 1 \cdot 1 \\
 &= 0 + 1 \\
 &= 1
 \end{aligned}$$

## Abstraction in Digital Design

Goal: build an 8-bit adder



# Sum-of-products

Sum of two 1-bit numbers

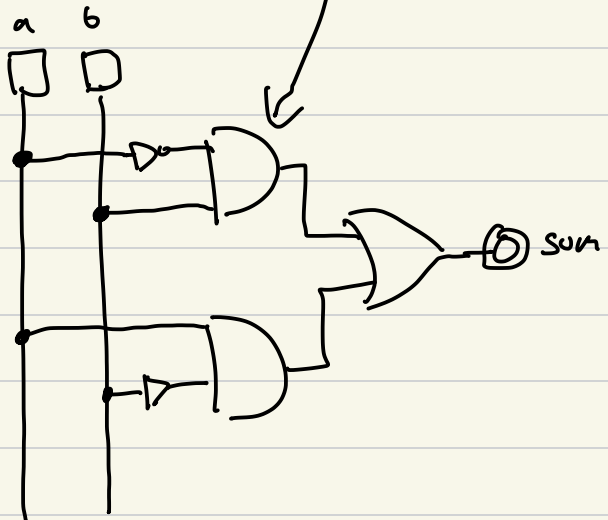
|   |   | XOR |      |
|---|---|-----|------|
| a | b | Sum | Cost |
| 0 | 0 | 0   |      |
| 0 | 1 | 1   |      |
| 1 | 0 | 1   |      |
| 1 | 1 | 0   |      |

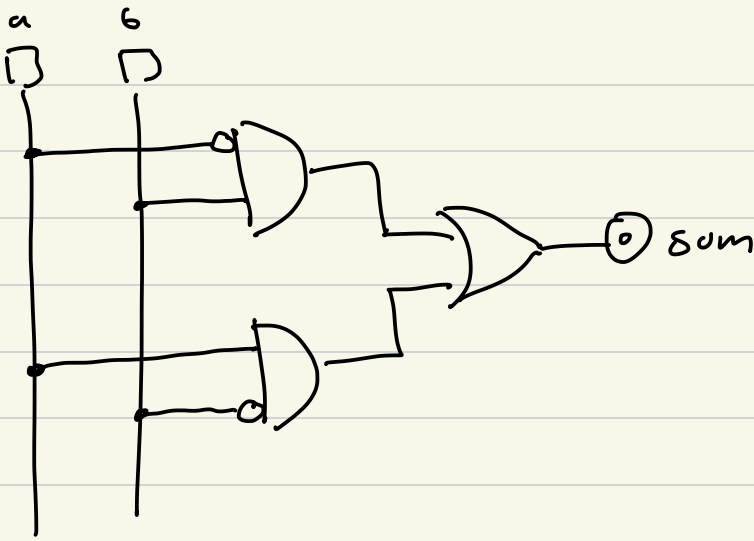
XOR  
 $sum = a \oplus b$

$$sum = (\bar{a} \cdot b) + (a \cdot \bar{b})$$

$$a = 0 \quad b = 1$$

$$\begin{aligned} sum &= (\bar{0} \cdot 1) + (0 \cdot \bar{1}) \\ &= (1 \cdot 1) + (0 \cdot 0) \\ &= 1 + 0 \\ &= 1 \end{aligned}$$





Sum. of - products

1) Build a truth table for your function

For each output

2) Identify rows with output of 1

3) Construct product (·) terms for each row

a) if input is 1  $\rightarrow$  do not invert var

b) if input is 0  $\rightarrow$  invert var

4) Sum (+) all product terms